

100

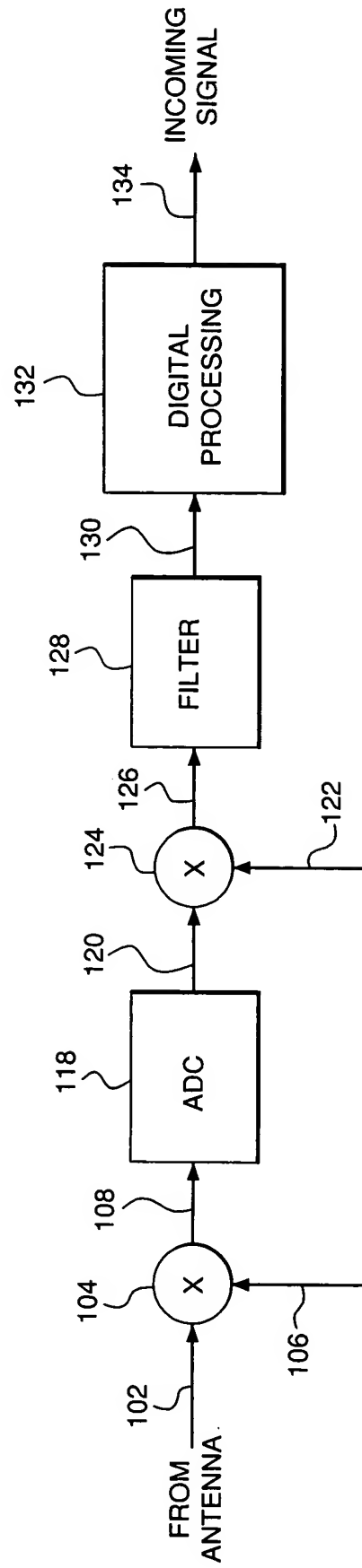
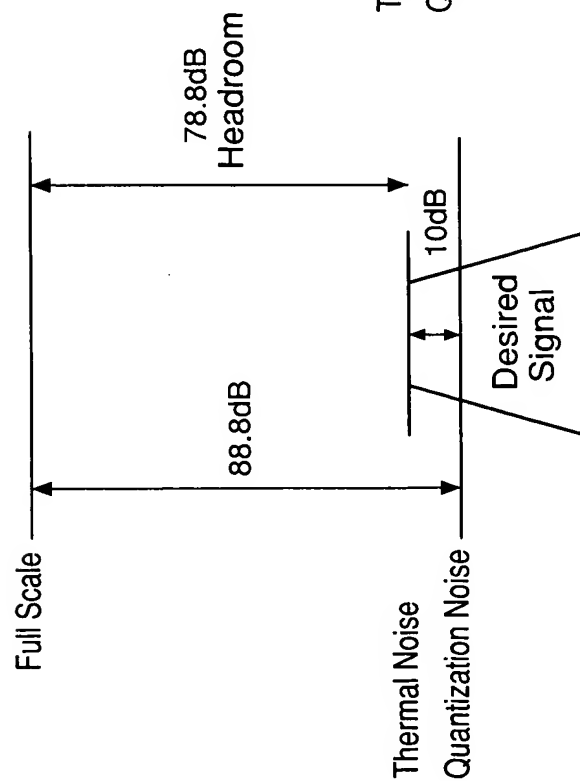
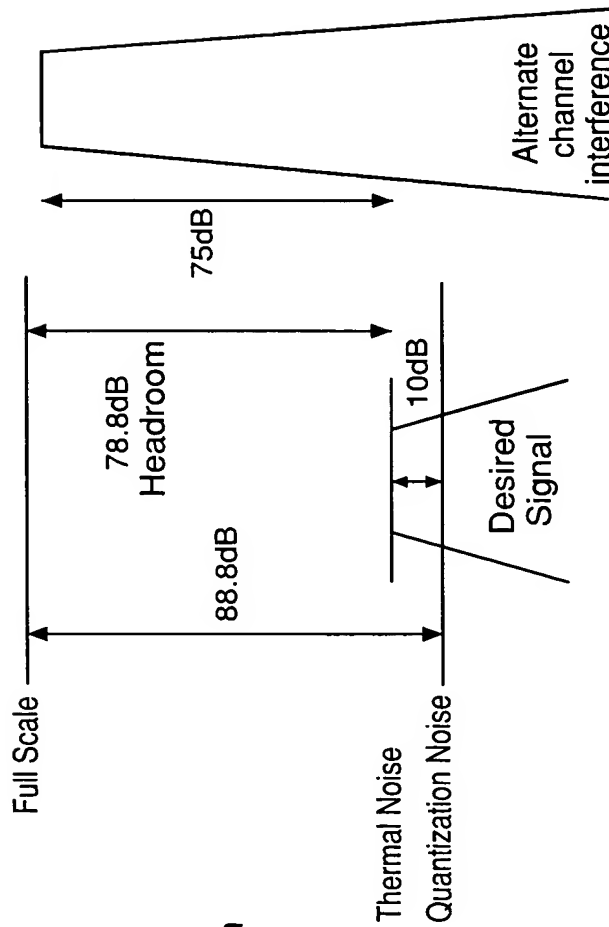


FIG. 1
(PRIOR ART)



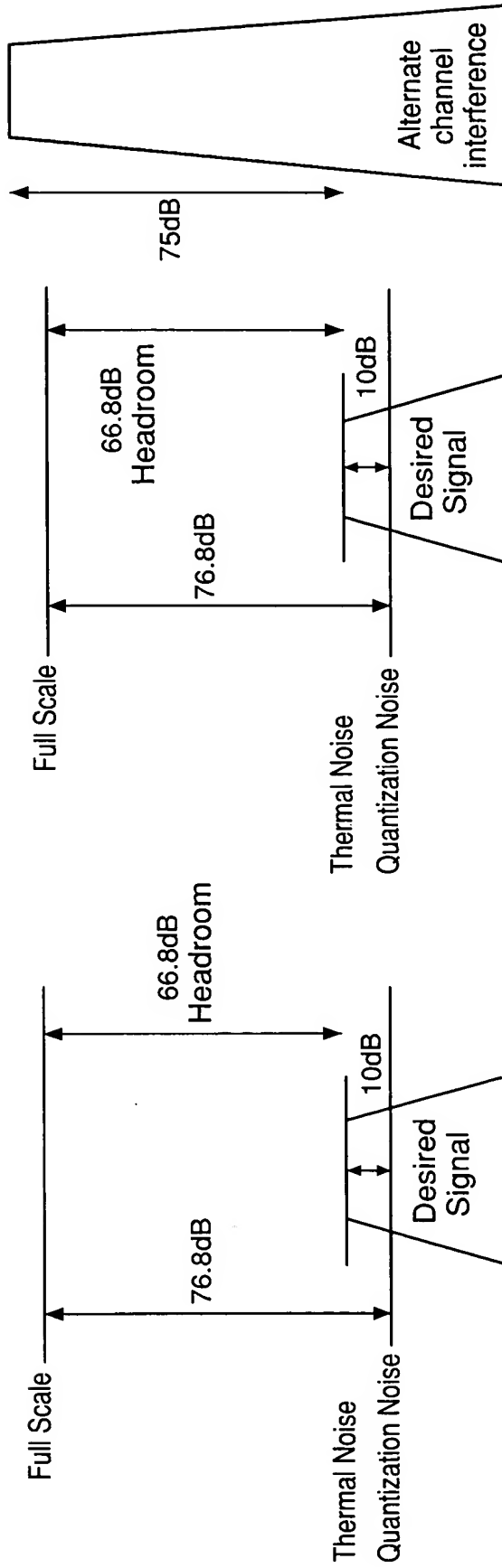
14-bit ADC at Interference Test Level
18 dB above sensitivity, no interference
SNR = 0dB

FIG. 2
(PRIOR ART)

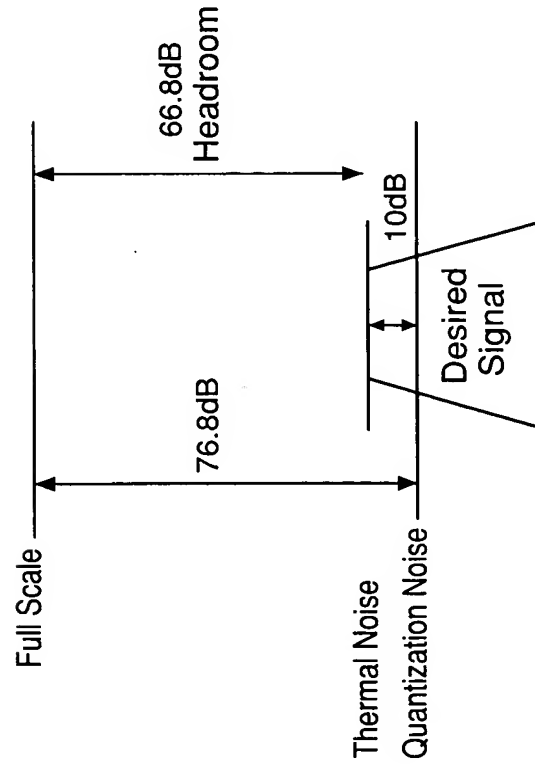


14-bit ADC at Interference Test Level
18 dB above sensitivity with interference
SNR = 0dB

FIG. 3
(PRIOR ART)



12-bit ADC at Interference Test Level
18dB above sensitivity with interference
SNR = 0dB



12-bit ADC at Interference Test Level
18dB above sensitivity, no interference
SNR = 0dB

FIG. 5
(PRIOR ART)

FIG. 4
(PRIOR ART)

600

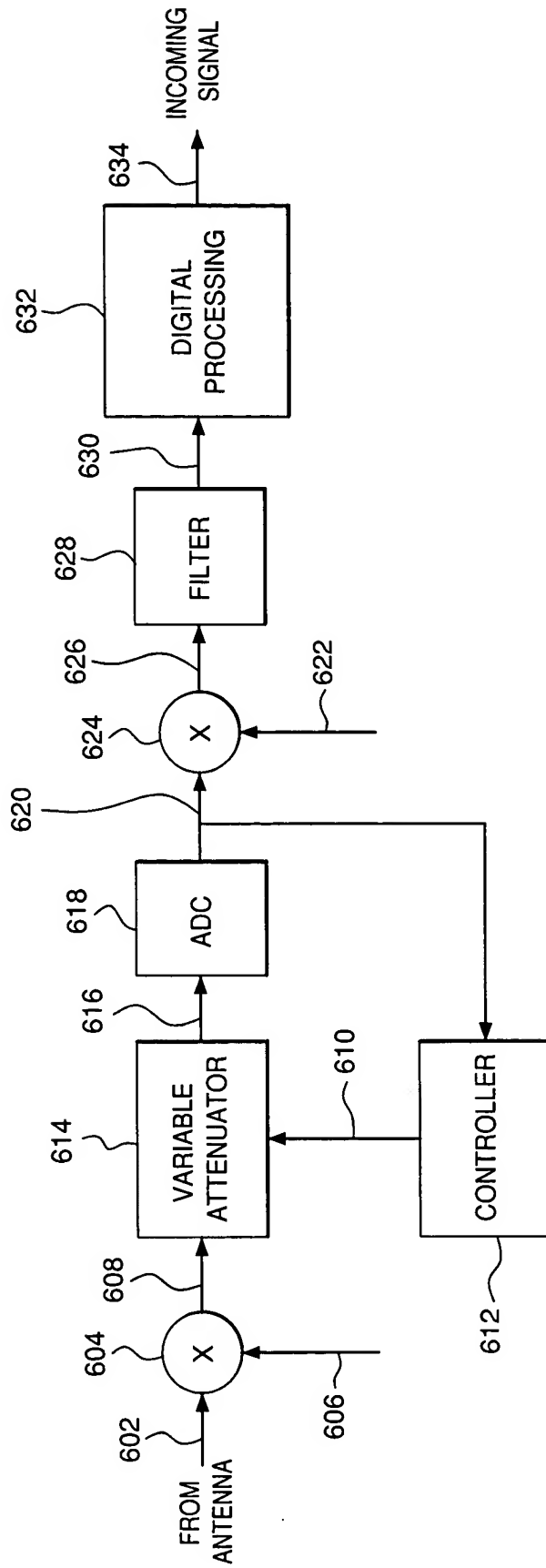


FIG. 6

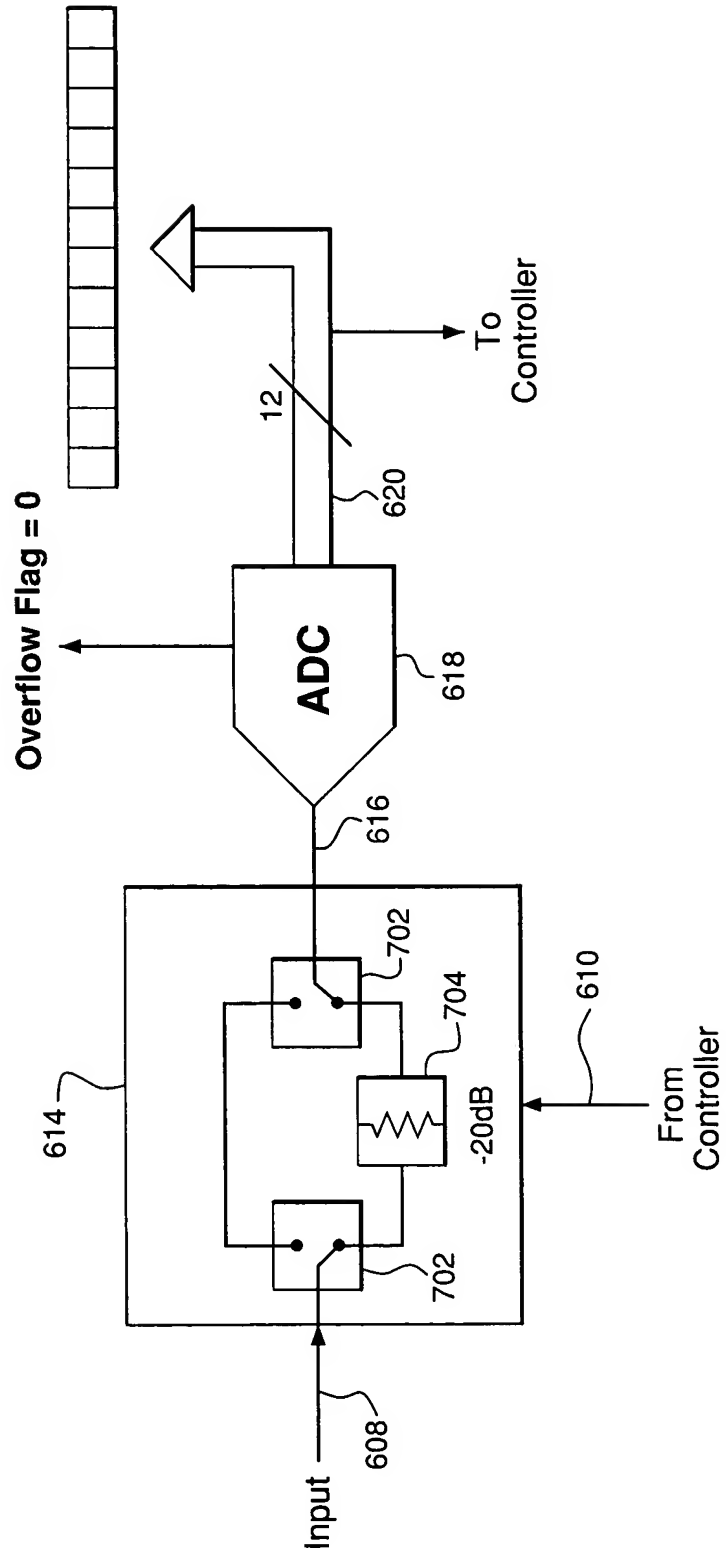
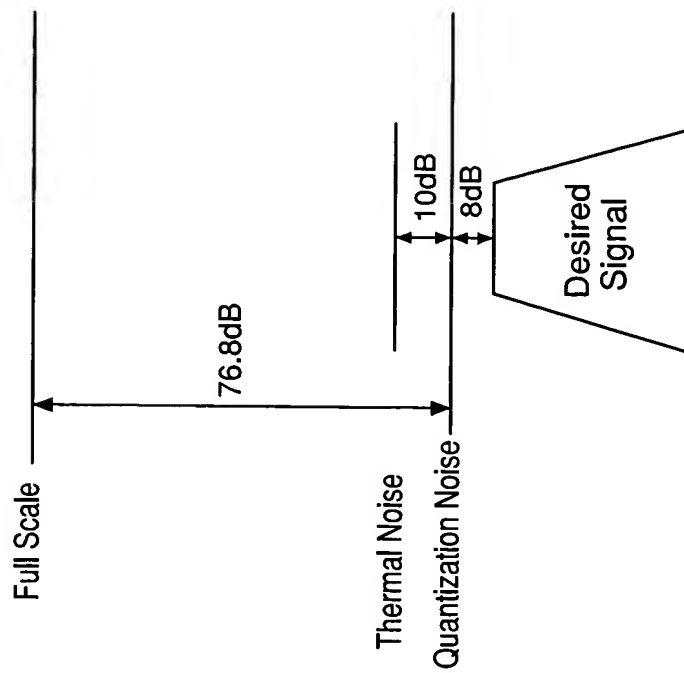
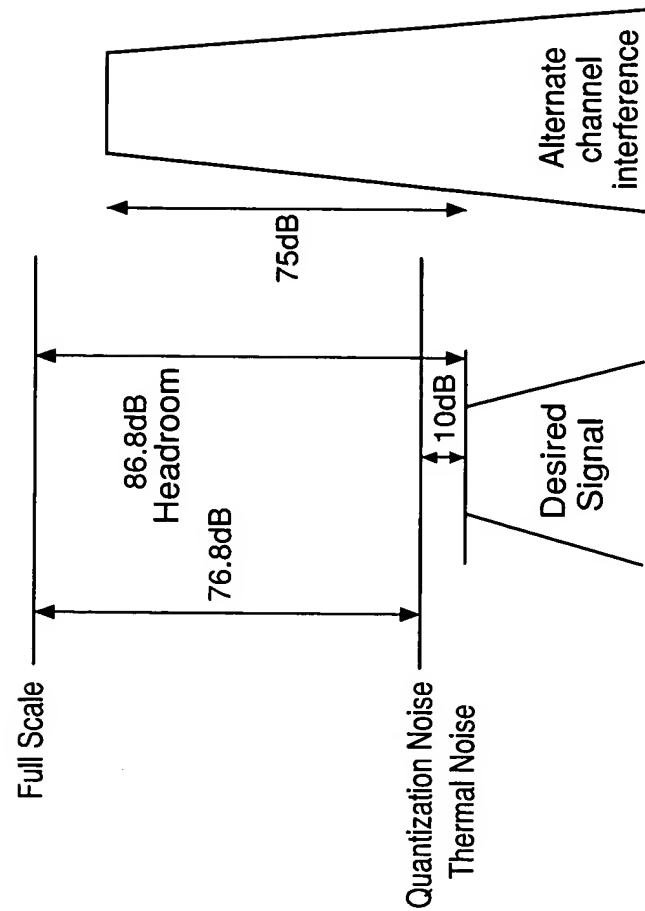


FIG. 7



12-bit ADC at Sensitivity
SNR = -18dB



12-bit ADC at Interference Test Level
18dB above sensitivity
ADC saturation sensed and 20dB attenuation added
SNR = -10dB

FIG. 8

FIG. 9